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Patentanmeldung Nr. | Patent application No. | Demande de brevet n°

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Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office

Le Président de l'Office européen des brevets
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Page 2 de l'attestation

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Device and method for subfield coding

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Remarques:

Device and method for subfield coding

The invention relates to a device for subfield coding as specified in the preamble of claim 1.

The invention also relates to a method for subfield coding as specified in the preamble of claim 8.

Such a device for subfield coding is used in large television displays and computer displays, which displays comprises a number of light sources arranged in a matrix. Such a display can comprise a plurality of LED's or can comprises a plasma display panel.

In the known device the subfield coding is applied to obtain a grey scale by means of a pulse width modulation of the light emitting diodes (LED's) in the display. The known device comprises means for subdividing every field of an image signal to be displayed into 255 subfields and processing means arranged to program all picture elements of the display device to emit or not to emit light during that subfield. For LED displays the brightness of the LED's are dependent on the LED current and the on-period of the LED's of the respective subfields. Accordingly, a picture element that should produce 1/256 of the maximum light-output will only emit light during one subfield and a pixel element that should produce 10/256 of the maximum light output will emit light during ten subsequent subfields. A disadvantage of this subfield coding is that it provides only a small dynamic range which is not sufficient for typical applications of large television and monitor screens in, for example, open air or relatively dark control rooms.

Another possibility is to increase the length of each of subsequent subfields with a predetermined amount as is applied in, for example, plasma display panels.
If, for example, every field of an image signal is subdivided in twelve subfields and the first subfield corresponds to 1/2048 of the maximum light output, the second subfield corresponds to 1/1024 of the light output and so on, a twelve bit grayscale can be obtained. However, a disadvantage of this subfield coding reduces the maximum obtainable brightness of the

display, because the light emitting elements do not emit radiation for the maximum possible time in a subfield in order to display the maximum brightness.

It is an object of the invention to provide a device for subfield coding which improves the dynamic range of a picture and maintains the maximum obtainable brightness of the image to be displayed. This object is achieved by a device according to the invention as specified in claim 1. In this device the predetermined total number of subfields having a fixed duration, is divided in the first number of subfields having a different period wherein the pixel element emits radiation and the second number of subfields having a fixed period in which the pixel element emits radiation. The first number of subfields are used to obtain a fine scale for the lower values of brightness of the picture element. The second number of the subfields are used to obtain a linear scale for the higher values of brightness of the picture element.

For example, when the total number of subfields is 256, a binary order or an order defined by successive negative powers of two can be applied to define the lower values of brightness for the first ten subfields. The relative length of the on-period of the first sub-field is 2 to the power - 10, the relative length of the second subfield is 2 to the power -9 and so on. The relative length of the 10^{th} period is then ½. The remaining 245 subfields have a fixed relative length of 1 and are used to obtain a linear scale for the higher values of brightness. So, the maximum obtainable brightness is hardly reduced. Applying this subfield coding in a display device improves the dynamic range of a large display so that it can operate under different ambient light conditions varying from low ambient brightness to high ambient brightness, while the maximum obtainable brightness of the device is maintained.

It is a further object of the invention to provide a method for subfield coding which improve the dynamic range of a picture and maintaining the maximum obtainable brightness of the image to be displayed. This object is achieved by the method for subfield coding according to the invention as specified in claim 8.

Further advantageous embodiments of the subfield coding device according to the invention are specified in the dependent claims.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

In the drawing:

Fig 1 shows a block diagram of a LED data display device and

Fig 2 shows a block diagram of a subfield coding device.

Fig 1 shows a block diagram of a LED data display device 6 comprising a system controller 1, a data distribution device 2, a data processing device 3, a driver device 4 and a display screen 5 comprising a plurality of LED's arranged in a matrix. The system controller 1 preferably comprises a micro-controller 10 for controlling the display device, an address mapping unit 11 for generating addresses to store the video data and a video data transfer unit 12 for transferring video data to the data distribution device 2. Furthermore, the system controller 1 comprises two inputs, one input 13 connected to the micro-controller 1 for sending and receiving control data and one input 14 connected to the video data transfer unit for receiving digital video data from a digital video source, for example, a digital video recorder, a digital video player or a personal computer. The system controller 1 is connected via two busses 15,16 with the data distribution device 2. A control bus 15 is used for communicating control data to and from the data distribution device 2 and a data bus 16 is used for transferring digital video data to the data distribution device 2. The data distribution device 2 comprises a communication channel 20 for communication of control data from the system controller 1 to the data processing device 3 and an image data channel 21 for transferring video data to the data processing device 3. A number of data distribution devices 2 can be used in dependence of the number of picture elements and the screen size of the display screen 5. Diagnostic and other data from the data distribution device 2 can also be transferred via the control bus 15 to the system controller 1. The data distribution device 2 is connected to the data processing device 3. The data distribution 2 device reformats the video data and distributes the video data to the data processing device 3. The data processing device 3 comprises a further micro-controller 31 and a subfield coding unit 32. The data processing device 3 is connected to the driver device 4 and generates a subfield-on signal 72 and an output-enable signal 73 to the driver device 4. The driver device 4 is connected to a portion of the display screen 5. The portion of display screen 5 comprises for example 16x16 picture elements. Each picture element comprises a red LED, a green LED and a blue LED. The driver device 4 comprise a latch 41,42,43 for storing the sub-field-on signal 72 for each of the 256 picture elements during each of the subfield. The subfield-on signal indicates whether a LED of the picture element emits or does not emit radiation for an actual subfield. The output- enable signal 73 determines the on-period of the LED's during each

subfield. The brightness of the LED's are dependent on the LED current provided by the driver device 4 and the on-period of the LED's of the respective subfields.

In practice, the number of picture element is 512 (horizontal) x 384 (vertical) x 3. So, that several driver devices 4, data processing devices 3 and data distribution devices 2 are necessary to address the full display screen 5.

Fig 2 shows an example of a block diagram of a subfield coding unit 32 for use in the data processing device 3. The subfield coding unit 32 comprises an image processing unit 60, a subfield counter 64, look up tables 65,66,67, period counters 68,69,70 and a clock circuit 71. Furthermore, the driver device 4 and a portion of the display screen 5 with two LED's (51,52;53,54;55,56) per colour are shown. In the subfield coding unit 32, the subfield counter 64 is connected to the image processing unit 60. In this example, the image processing unit 60 determines an eighteen-bit digital value from the eight-bit video data. Furthermore, 256 subfields are applied for the subfield coding of the display device 6. The first ten subfields have a different duration of the on-period, the remaining 245 subfield have a fixed on-period. The duration of an on-period of one of the first ten subsequent subfields is a function of the rank of a selected bit in the sequence of the ten least significant bits. The value of these ten respective least significant bits of the eighteen-bit digital value are used for the determination whether the LED emits or does not emit radiation in one of the first ten subsequent subfield which order corresponds to the order of the bit in the sequence. The subfield coding unit 32 comprises means 61 for determining the first number of subfields that emit radiation. Preferably, said means 61 determines which of the ten first subsequent subfields the LED emit radiation from the value of ten respective least significant bits of the eighteen-bit digital value. So, a first number of subfields having a different on-period is determined.

Furthermore, the subfield coding device 32 comprises means 62 for determining the second number of subfields having a fixed on-period from the remainder eight most significant bits of the eighteen-bit digital value. The second number of subfield is determined by the value formed by the eight most significant remaining bits of the digital value. The second number equals the number of remaining subfields wherein the LED emits radiation for the whole duration of the subfield. In this example the second number is at maximum 245 subfields. In this way, the means for determining the second number of subfields determines the second number of subfields having a fixed period wherein the LED emit radiation and generates a subfield-on-signal 72 for those subfields. This method for subfield coding provides a large dynamic range while the maximum brightness of the display screen is substantially maintained.

Both means 61, 62 generates a sub-field-on-signal 72 for the individual LED's associated to the subfield coding device 32. The sub-field-on signal 72 is send via a logical OR circuit 63 to the driver device 4. Furthermore, the image processing unit 60 generates a subfield-count-signal 78 and a frame-reset-signal 77 which indicates the beginning of a new frame. The subfield counter 64 counts the number of subfields from the sub-field-count signal 78 and is reset to zero when the frame-reset-signal 77 is received. An output of the subfield counter 64 is connected to an input of the look-up tables 65,66,67. For each colour, a look-up table 65,66,67 and period counter 68,69,70 is present. The outputs of the respective look-up tables 65,66,67 are connected to the respective period counters 68,69,70 for loading a digital number in the respective period counter. The period counters 68,69,70 generates the output-enable-signal 73,74,75 for the respective red, green and blue LED drivers 41,42, 43 in dependence of the loaded digital number and a clock signal 79. After receiving the frame reset signal 77 the subfield counter 64 counts the consecutive subfields. Preferably, the clock frequency of the clock signal 79 is 10 Mhz to obtain a sufficient low output of the LED's under low ambient light conditions for PAL TV images. The clock circuit 71 generates the clock signal 79. Furthermore, the subfield counter 64 and the period counters 68,69,70 should be reset with the frame reset signal to avoid unwanted interference when the display screen 50 is being filmed. The output-enable signals 73,74,75 and the sub-field-on signal 72 are send to the screen driver 4 and stored in latches 41,42,43 having outputs connected to the LED's 51,52;5354;55,56.

The digital numbers for the look-up tables 65,66,67 can be determined along the following lines. For a PAL TV image each field is 20 ms, when the field is divided in 256 subfields. Each subfield takes 78.125 microsecond and the number 782 corresponds to the maximum number of clock periods defining maximum length of the period wherein the LED emits radiation in that subfield. For a NTSC TV image each field is 17 ms, when the field is divided in 256 subfield each subfield takes 65 microseconds and the decimal number 651 corresponds to the maximum number of clock periods defining the maximum length of the period wherein the LED emits radiation in that subfield. In this example, the look-up table has a table of 256 entries corresponding to the number of applied subfields and a 10-bit output for coding 782 different lengths varying from 1 to 782 clock periods defining the on-period in which the LED emits radiation in an associated subfield.

An example of a lookup table containing the decimal numbers for the period counters 68,69,70 is shown in table 1.

Table 1

0	2
1	2
2	3
3	6
4	12
5	24
6	49
7	97
8	195
9	391
10	782
..	782
255	782

In the table, for practical reasons the minimal on-period for the LED is restricted to at least 2 counts.

In operation, the image processing unit 60 converts the 8-bit digital video data Iin in a 18-bit number value Iout via a gamma correction. For example, the gamma correction is represented by the function Iout= (Iin)^gamma * (Max/ MaxIin^gamma), wherein

Gamma =2.2,

Max represents a maximum eighteen-bit value and

MaxIin represent the maximal value of the video data.

The first eight most significant bits of the value Max are determined by the decimal number 245 and the ten least significant bits of the value Max are determined by the decimal number 1023. This eighteen-bit binary value Max represents the decimal value 251903.

Furthermore, the values for the look-up table 65,66,67 may include this gamma correction. The values of the look-up table may also include other non-linear image processing functions to compensate for a non-linear brightness scale.

Furthermore, the look-up tables 65,66,67 may be equal to each other. In order to provide a white point correction to obtain a desired colour balance between the red, green and blue, the values in the different look-up tables can be altered.

Furthermore, the look-up tables 65,66,67 can be loaded with pre-stored data, but alternatively it may also possible to load new tables in the look-up-table via an external computer and the control bus 15.

It should be noted that the above mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative solutions without departing from the scope of the claims. In the claims enumerating several means, several of these means can be embodied by one and the same item of hardware. The invention is preferably applied in large screen LED displays for outdoor use and other matrix displays (digital micro mirrored device, plasma display panel (PDP)) but can also be applied with other devices such as O-LED displays devices in mobile telephones.

CLAIMS:

1. Device for subfield coding a large screen display comprising means for generating a digital value for a pixel element of the display and means for determining a first number and a second number of subfields of a predetermined total number of subfields from the digital value for which determined subfields the picture element emits radiation
characterized in that the device comprises
- means for determining a first number of subfields having a fixed duration and a different on-period in which the picture element emits radiation from the digital value and
- means for determining the second number of subfields from the digital value whereby the second number of subfields have a fixed duration and a fixed on-period during which the picture element emits radiation.

2. Device for subfield coding as claimed in claim 1 characterized in that the means for determining the first number are arranged to determine the first number from a sequence of predetermined number of least significant bits of the digital value.

3. Device for subfield coding as claimed in claim 2 characterized in that the subfield coding device comprises means for generating a subfield sequence having a number of subfields equal to the determined number of least significant bits and the duration of the on-period of a subfield selected from the subfield sequence is a function of two to the power of the rank of the selected subfield in the subfield sequence.

4. Device for subfield coding as claimed in claim 3 characterized in that the subfield coding device comprises means for generating an output-enable signal depending on the order of a subfield in the subfield sequence and the value of a bit in the sequence of least significant bits, which order of the bit in the sequence corresponds to the order of the subfield.

5 Device for subfield coding as claimed in claim 4 characterized in that the means for generating the output enable signal comprises a look-up table and an period counter for counting the length of the on-period.

6. Device for subfield coding as claimed in claim 1 characterized in that the means for determining the second number are arranged to determined the second number proportional to a value formed by the remainder of most significant bits of the digital value.

7. Display device comprising a display screen having a plurality of controllable light sources arranged in a matrix comprising a device for subfield coding as claimed in claim 1.

8. Method for subfield coding a large screen display comprising the steps of generating a digital value for a pixel element of the display and
determining a first number and a second number of subfields from a predetermined total number of subfields from the digital value for which determined subfields the picture element emits radiation
characterized in that the step of determining subfields comprises
- a sub-step for determining a first number of subfields having a fixed duration and a different on-period in which the picture element emits radiation from the digital value and
- a sub-step for determining the second number of subfields from the digital value whereby the second number of subfields have a fixed on-period during which the picture element emits radiation.

ABSTRACT:

The invention relates to a device for subfield coding a large screen display. The device comprises means for generating a digital value for a pixel element of the display and means for determining a first number and a second number of subfields from a predetermined total number of subfields from the digital value for which determined subfields the picture element emits radiation. In order to improve the dynamic range while maintaining the maximal obtainable brightness the device comprises means for determining the first number of subfields from the digital value, whereby the subfields have a different on-period during which the picture emits radiation and means for determining the second number of subfields from the digital value whereby the subfields have a fixed on-period during which the picture element emits radiation.

Fig. 2

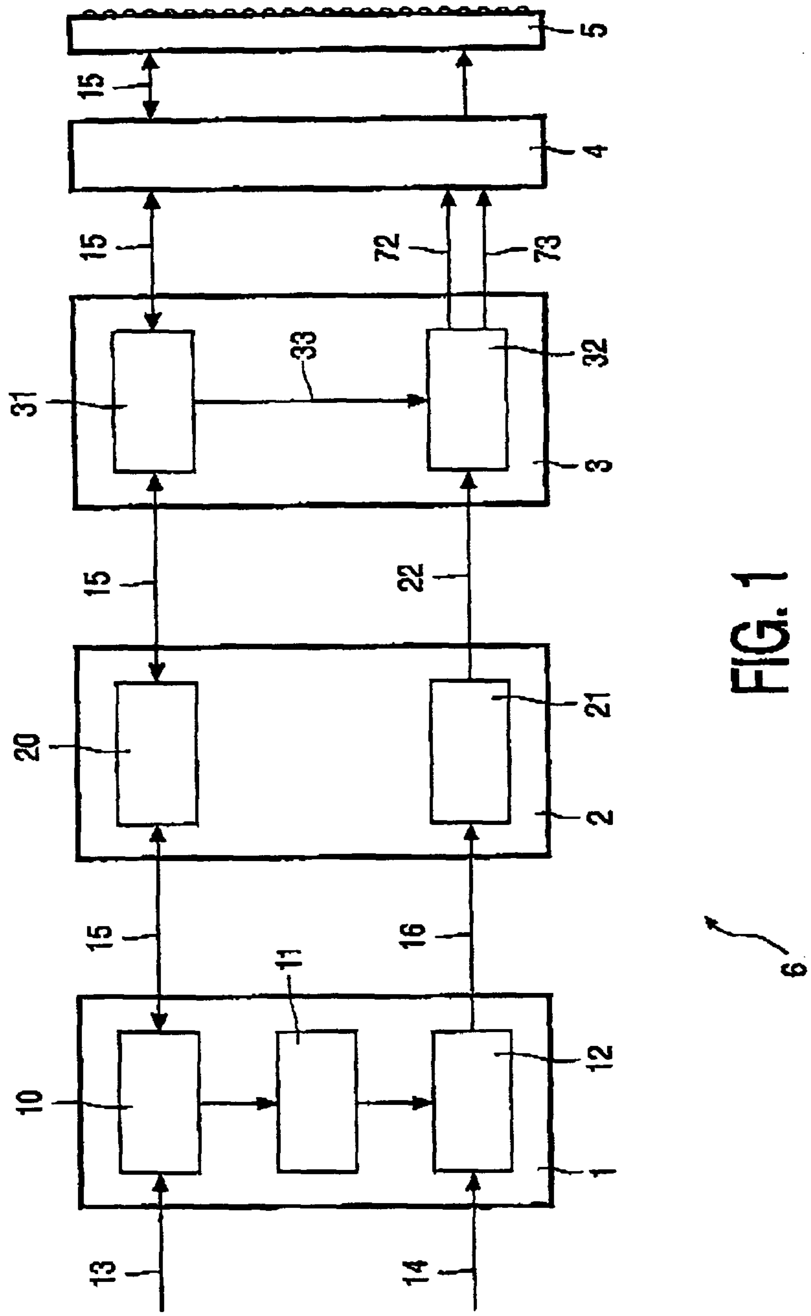

FIG. 1

2/2

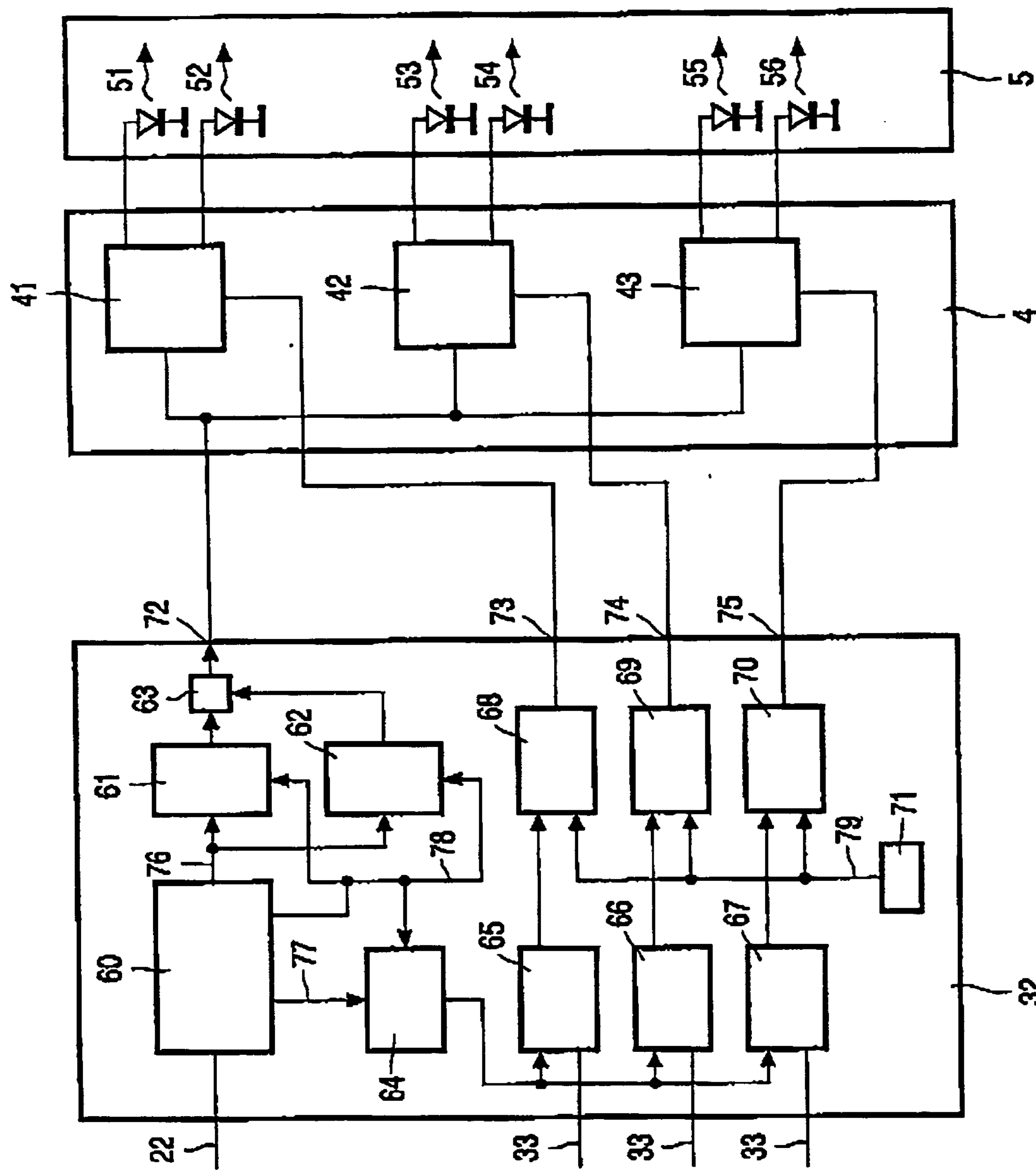
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FIG. 2